

IN THE SPECIFICATION

Please revise the paragraph starting at page 9, line 7 as follows:

FIG. 4 is an exemplary circuit diagram of a backlight assembly 900 according to an embodiment of the present invention, and FIG. 5 is a graph illustrating an output signal of an exemplary comparator as function of an input voltage. Furthermore, FIGs. 6A and 6B are graphs respectively illustrating a current flowing through a lamp and varying based on the ~~hysterisis~~ hysteresis characteristic according to an embodiment of the present invention.

Please revise the paragraph starting at page 10, line 7 as follows:

The comparing block 9442 includes a comparator COM1 functioning as a Schmitt trigger having a ~~hysterisis~~ hysteresis characteristic and having a non-inverting terminal (+) and an inverting terminal (-), a voltage divider for generating a reference voltage V_{ref} to be supplied to the inverting terminal (-) of the comparator COM1, and an RC circuit for smoothing a voltage supplied to the non-inverting terminal (+) of the comparator COM1. The RC circuit includes a resistor R13 and a capacitor connected between the resistor R13 and a ground and it is connected to the non-inverting terminal (+) of the comparator COM1 through an input resistor R14. The voltage divider includes a pair of resistors connected in series between a supply voltage V_{dd} and a predetermined voltage such as a ground. The comparator COM1 has a positive feedback connection through a feedback resistor R 16 and a resistor R15 is connected between the non-inverting terminal (+) and a predetermined voltage such as a ground. The comparator COM1 may be a non-inverting type ~~hysterisis~~ hysteresis comparator.

Please revise the paragraph starting at page 12, line 10 as follows:

A comparator COM1 as well as the resistors R15 and R16 used for a comparing unit according to an embodiment of the present invention has the ~~hysterisis~~ hysteresis characteristic as shown in FIG. 5, which is a graph illustrating an output signal of a comparator as function of an input voltage. That is, the output of the comparator COM1 is different between an increasing non-inverting input and a decreasing non-inverting input. In detail, a current restriction establishment voltage V_{thh} at which the output of the comparator COM1 changes from a low state to a high state is higher than a current restriction release voltage V_{thl} at which the output of

the comparator COM1 changes from a high state to a low state. The ~~hysteresis~~ hysteresis characteristic of the comparator COM1 reduces noises and unstable operation due to frequent operation changes between the current restriction state and the normal current state.